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# A CAS MASTER TEACHER CPD SESSION

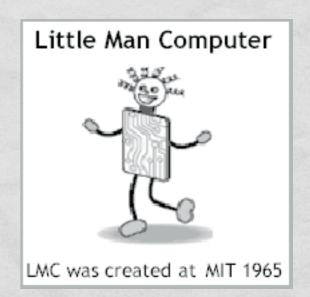
#### **ADMIN ITEMS**

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16:15 - Welcome 16:30 - Introduction 16:45 - Session I - Simple Programs 17:15 - Session 2 - Branching 17:45 - Session 3 - Application 18:15 - Plenary pi.mwclarkson.co.uk

### LITTLE MAN COMPUTER

The Lord And State of The State



A small man (or woman) 100 empty mailboxes 1 instruction at a time

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### **INSTRUCTION SET**

State Block of Marsh & Cha

INP OUT STA variableName LDA variableName ADD variableName SUB variableName HLT variableName DAT

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Get some **input** from the user **Output** the current value to the user **Store** the current value in variableName **Load** the value in variableName Add the value in variableName **Subtract** the value in variableName Halt (end) the program Declare variableName as data

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### THE ACCUMULATOR

INP Input to the accumulator OUT Output from the accumulator STA variableName Store from the accumulator to variableName LDA variableName Load into the accumulator from variableName ADD variableName Add variableName to the accumulator SUB variableName Subtract variableName from the accumulator HLT Halt (end) the program variableName DAT Declare variableName as data COMPUTING AT SCHOOL pi.mwclarkson.co.uk ATE · ENGAGE · ENCOURAGE

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### WHAT WILL THIS DO?

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Message Box: INP STA numOne

INP STA numTwo

I DA numOne ADD numTwo

STA numThree

OUT

HIT

numOne DAT numTwo DAT numThree DAT

Input a number & store it as numOne Input a number & store it as numTwo Load numOne & add numTwo to it Store the answer as numThree Output the answer & stop Variable declaration (always goes last) **COMPUTING AT SCHOOL** pi.mwclarkson.co.uk EDUCATE · ENGAGE · ENCOURAGE

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### TEST IT OUT!

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Message Box: INP STA numOne

INP STA numTwo

LDA numOne ADD numTwo

STA numThree

OUT

HLT

numOne DAT numTwo DAT numThree DAT

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#### BRANCHES

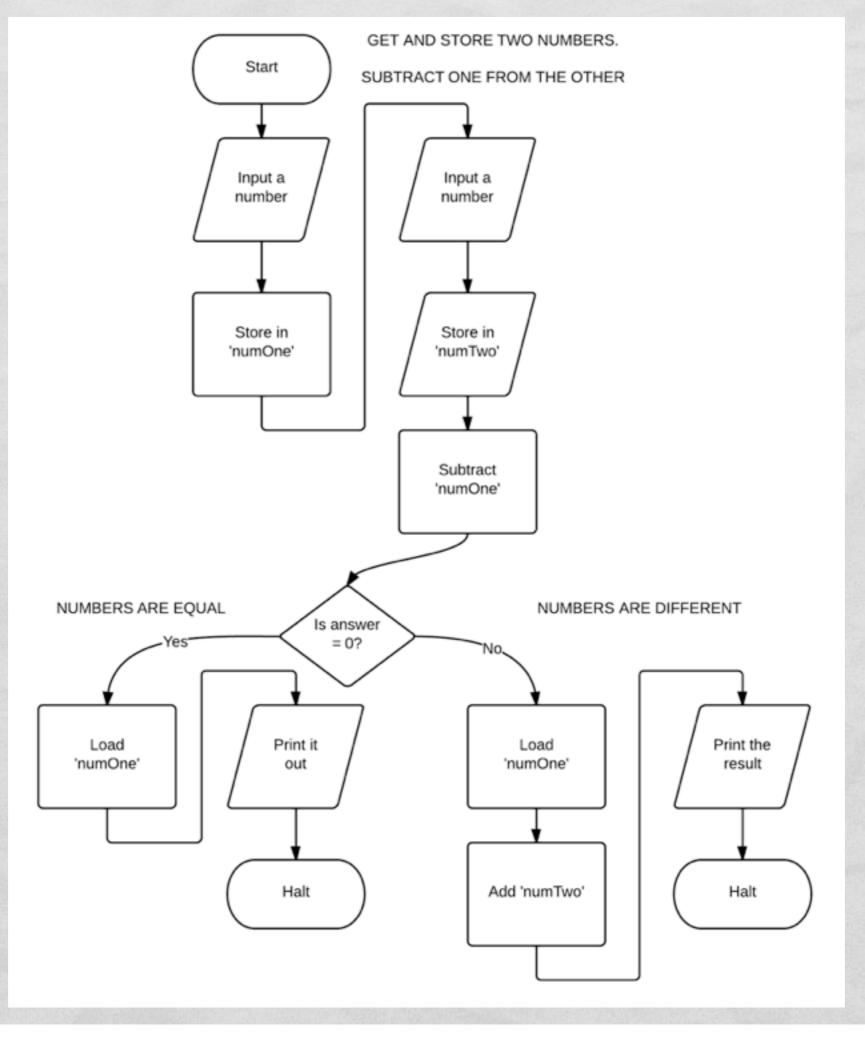
Strate Block owned a Chevel to

16g.

	Code	Meaning	
	BRZ	Branch if zero	
	BRP	Branch if positive (or zero)	
	BRA	Branch always (used for looping)	
tes.			
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### **APPLICATION TO THEORY**

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Introduction to programming - Sequencing Introduction to programming - Variables Representation of data - Instructions Hierarchy of programming languages Translators - Compilers, Interpreters & Assemblers Fetch-Execute Cycle **COMPUTING AT SCHO** pi.mwclarkson.co.uk TE · ENGAGE · ENCO

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#### SEQUENCING

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If you can dodge a wrench, you can dodge a ball

#### VARIABLES

AND REAL AND A COMPANY



#### **REPRESENTATION OF DATA**

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```
---- Translating Mnemonics -----
Line 0 : INP
  Opcode = 901
Line 1 : STA
  Opcode = 3 Address = 08
Line 2 : INP
  Opcode = 901
Line 3 : STA
  Opcode = 3 Address = 09
Line 4 : LDA
  Opcode = 5 Address = 08
Line 5 : ADD
  Opcode = 1 Address = 09
Line 6 : OUT
  Opcode = 902
Line 7 : HLT
  Opcode = 0
Line 8 : DAT
Line 9 : DAT
---- Program Successfully Compiled -----
```

Instruction	Mnemonic	MachineCode
Load	LDA	5xx
Store	STA	3xx
Add	ADD	1xx
Subtract	SUB	2xx
Input	INP	901
Output	OUT	902
End	HLT	000
Branch if zero	BRZ	7xx
Branch if zero or positive	BRP	8xx
Branch always	BRA	6xx
Data storage	DAT	

## HIERARCHY OF PROGRAMMING LANGUAGES

High-Level Language e.g. Python / Java / VB	numOne = numOne + numOne	
Low-Level Language e.g. Assembly Language	LDA numOne ADD numOne STA numOne	
Machine Code	0011 1100 0001 1100 0011 1100	
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#### TRANSLATORS

A State Contraction of Strate

High-Level Langu e.g. Python / Java /	
Low-Level Langua e.g. Assembly Lang	
Machine Code	00111100 00011100 00111100
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### FETCH-EXECUTE CYCLE

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INP STA numOne INP STA numTwo LDA numOne ADD numTwo STA numTwo OUT HLT numOne DAT numTwo DAT

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When a pupil asks you if they're allowed to program in opcodes instead of mnemonics, you know that they've got it



